

DO. NO. 5038-063

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#12

Inventor(s): John B. Halbert et al.

Serial No.: 09/678,751

Examiner: Pierre M. Vital

Filing Date: October 3, 2000

Group Art Unit: 2188

Title: BUFFERED MEMORY MODULE SUPPORTING HIGH-SPEED SYSTEM
MEMORY BUS USING WIDER MEMORY MODULE BUS (as amended)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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INFORMATION DISCLOSURE STATEMENT

OFFICE OF PETITIONS

This Information Disclosure Statement is submitted:

under 37 CFR 1.97 (b); or

(Within three months of filing national application; or date of entry of International application; before mailing date of first office action on the merits; or before the mailing of a first Office action after the filing of a request for continued examination under §1.114; whichever occurs last)

under 37 CFR 1.97 (c) together with either a:

Statement under 37 CFR 1.97 (e), or
 a \$180 fee under 37 CFR 1.17 (p); or

(After mailing of first Office Action, but prior to Notice of Allowance or Final Office Action)

under 37 CFR 1.97 (d) together with:

Statement under 37 CFR 1.97 (e), and
 a \$180.00 fee set forth in 37 CFR 1.17 (p).

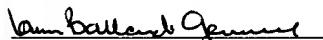
(Filed after final action or notice of allowance, whichever occurs first, but prior to payment of issue fee)

Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

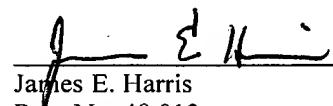
It is requested that the information disclosed herein be made of record in this application. Applicant notes that the Litaize (U.S. 6,345,321) reference is lengthy. The Examiner is particularly directed to Litaize Figures 20a and 20b and the accompanying text.

Any deficiency or overpayment to be charged or credited to deposit account number 13-1703.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 Date: December 10, 2003


Lauren Ballard-Gemmell

Respectfully submitted,
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PATENT APPLICATION
Do. No. 5038-63
Intel Ref. No. P9833

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re patent application of: John B. Halbert et al.

Serial No. 09/678,751

Examiner:

Pierre M. Vital

OFFICE OF PETITIONS

Filed: October 3, 2000

Group Art Unit:

2188

For: BUFFERED MEMORY MODULE SUPPORTING HIGH-SPEED SYSTEM
MEMORY BUS USING WIDER MEMORY MODULE BUS (as amended)

Confirm. No. 5518

TRANSMITTAL LETTER

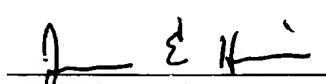
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Alexandria, VA 22313-1450

Enclosed for filing in the above-referenced application are the following:

- Request for Continued Examination
- Petition for Withdrawal From Issuance
- Paper No. 11 (Copies)
- Supplemental Information Disclosure Statement
- PTO Form 1449 References cited
- PTO Form 2038 authorizing credit card payment for (\$880.00)
- Return Postcard
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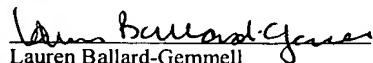
Respectfully submitted,

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